

Claims

I claim:

1. A method of protecting content by including two watermarks, a protect watermark which is difficult to remove and declares the content is protected, and a rights watermark which is efficient to embed or retrieve and declares the content's rights, thereby improving efficient and reducing costs of copy protection hardware.
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10. 2. The method of claim 1 in which rights watermark is difficult to duplicate.
15. 3. The method of claim 1 in which the embedding process adds the protect watermark when the content is created, and the protect watermark is copied with the content in each reproduction.
20. 4. The method of claim 3 in which the embedding process adds the rights watermark each time the content is reproduced.
25. 5. The method of claim 1 in which the embedding process adds the rights watermark each time the content is reproduced.
6. The method of claim 1 in which the retrieving process searches for the rights watermark each time the content is acted upon.
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7. The method of claim 6 in which the retrieving process enables the desired action if rights watermark is retrieved and contains the correct information.
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8. The method of claim 7 in which the retrieving process only searches for the protect watermark if the rights watermark is not found.
35. 9. The method of claim 8 in which the retrieving process enables the desired

action in the absence of the protect watermark.

10. The method of claim 1 in which the retrieving process enables the desired action if rights watermark is retrieved and contains the correct information.
11. The method of claim 1 in which the retrieving process only searches for the protect watermark if the rights watermark is not found.
10. The method of claim 1 in which the retrieving process enables the desired action in the absence of the protect watermark.
13. An apparatus consisting of a logic processor and storage unit implementing a means of embedding either the protect or rights watermark, or both.
14. The apparatus of claim 13 where the logic processor is a digital signal processor and the memory is digital random access memory.
15. An apparatus consisting of a logic processor and storage unit implementing a means of retrieving either the protect or rights watermark, or both.
20. The apparatus of claim 15 in which the apparatus contains a portable section and loader, and is designed such that the portable section never needs to check for the protect watermark, thereby reducing the computational power required on the portable section.
25. The apparatus of claim 15 where the logic processor is a digital signal processor and the memory is digital random access memory.
30. The apparatus of claim 15 where the logic processor is a digital signal processor and the memory is digital random access memory.